

Figure 1A

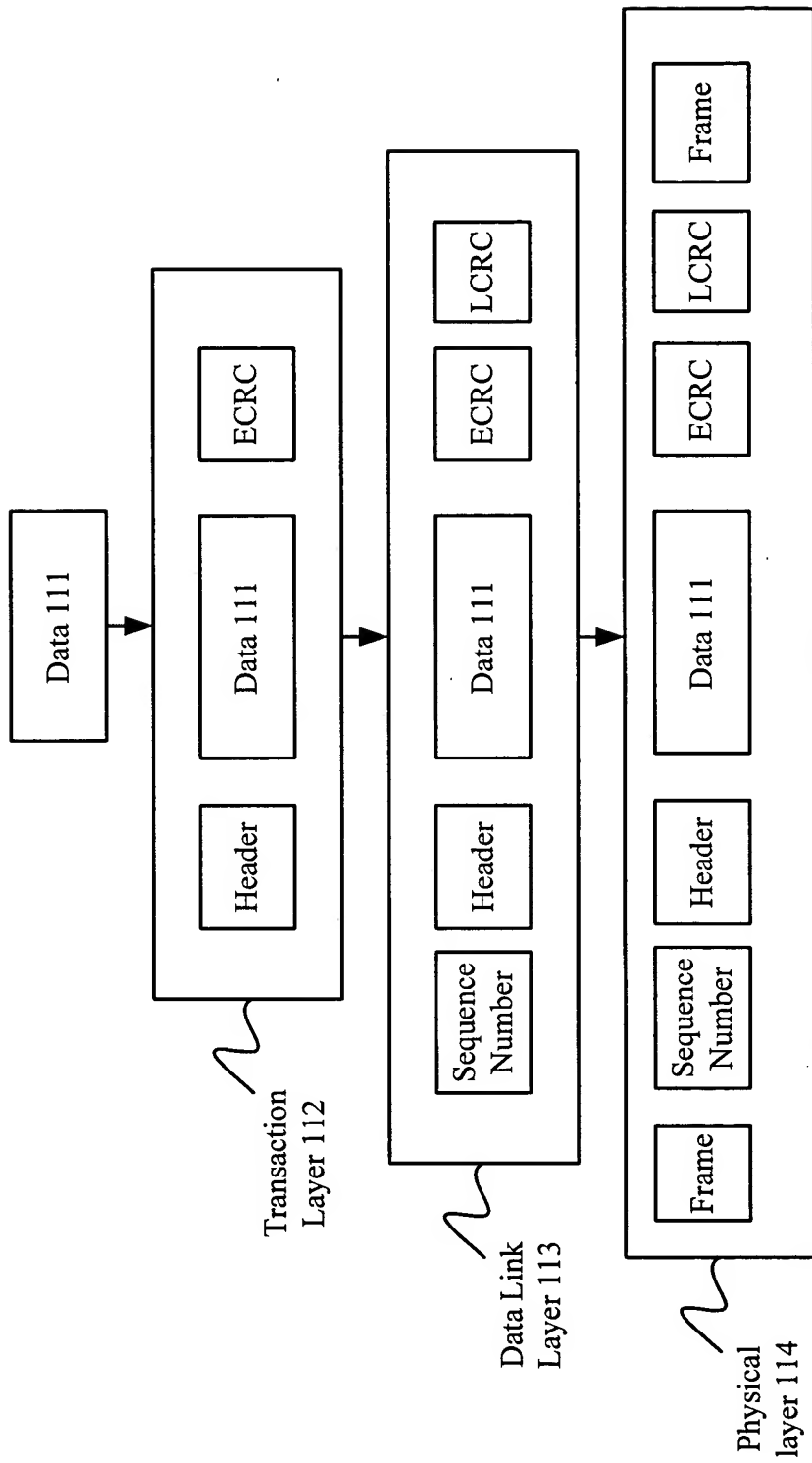


Figure 1B

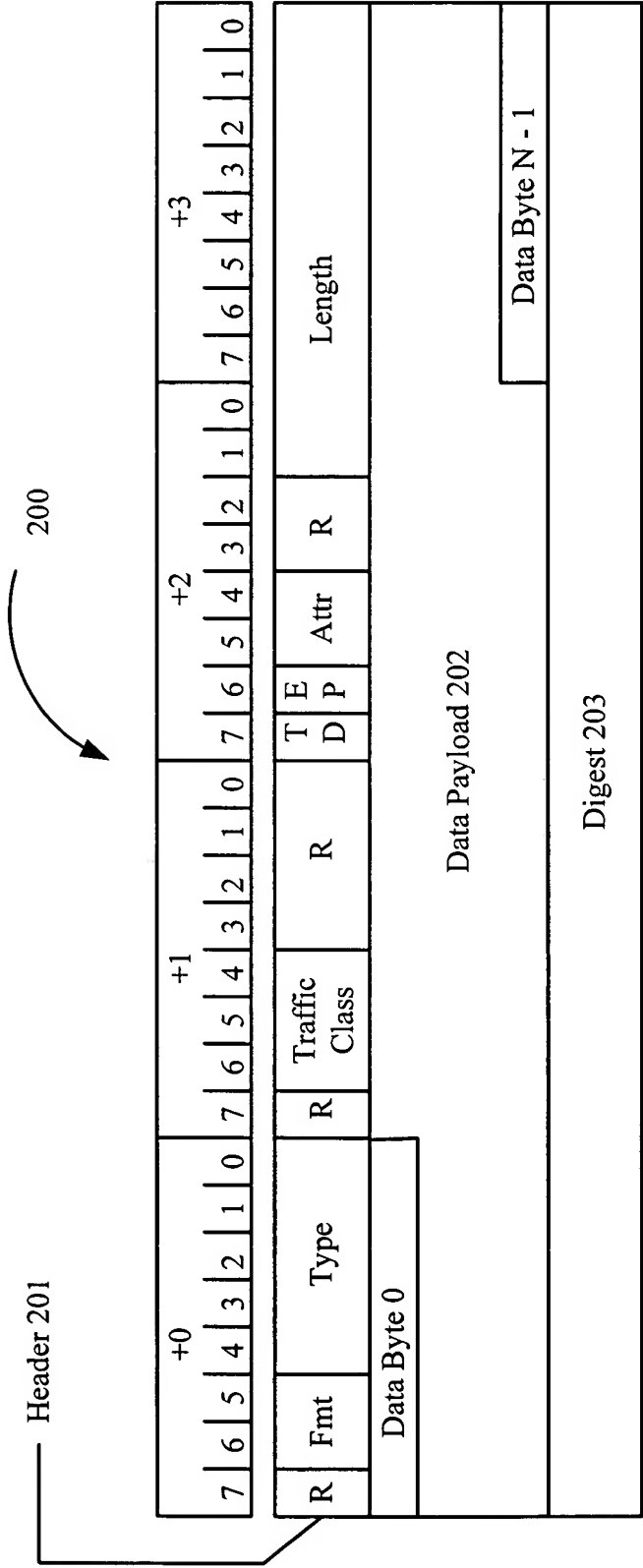


Figure 2

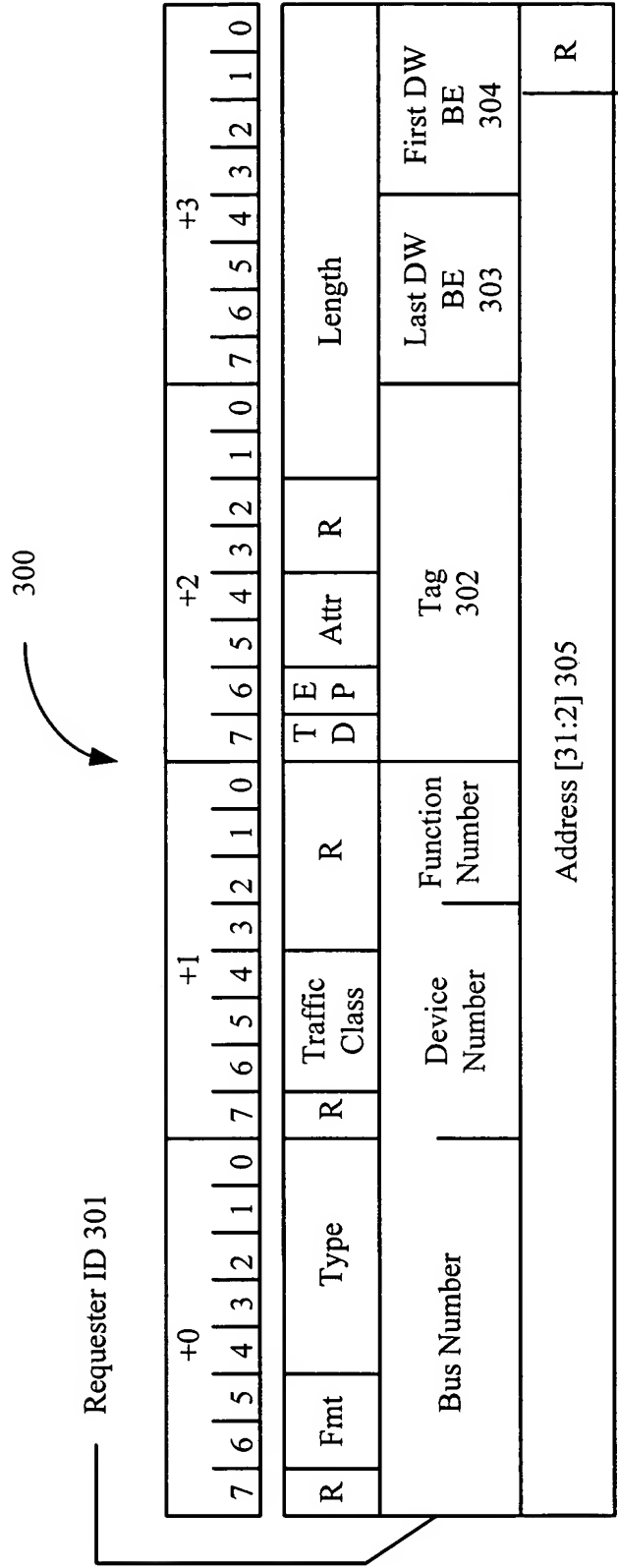


Figure 3

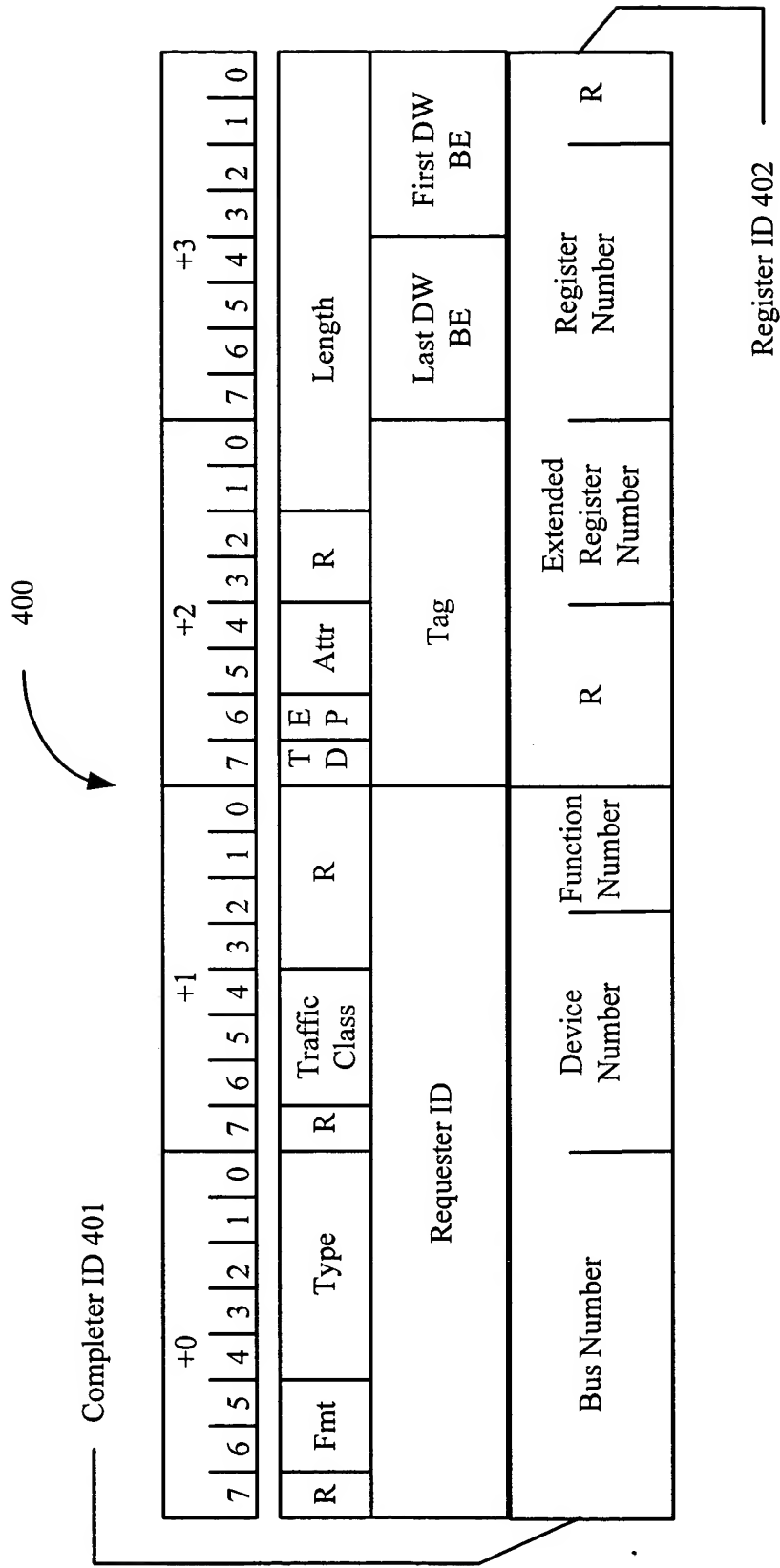


Figure 4

500

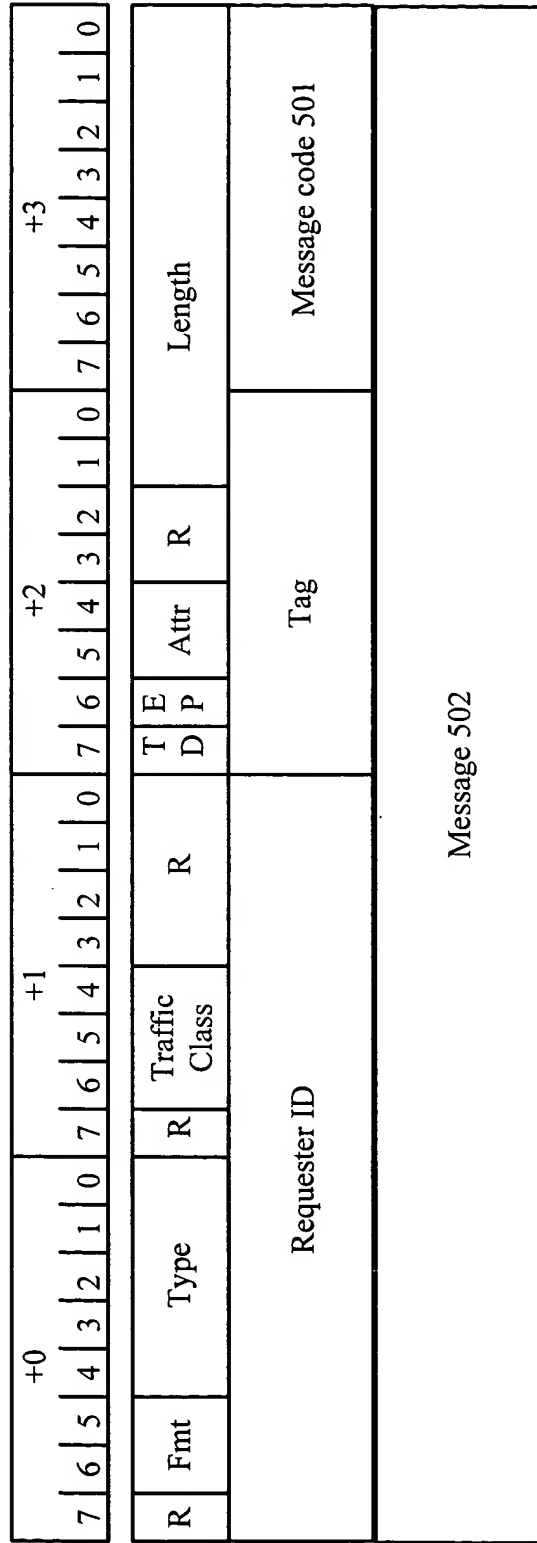


Figure 5

600

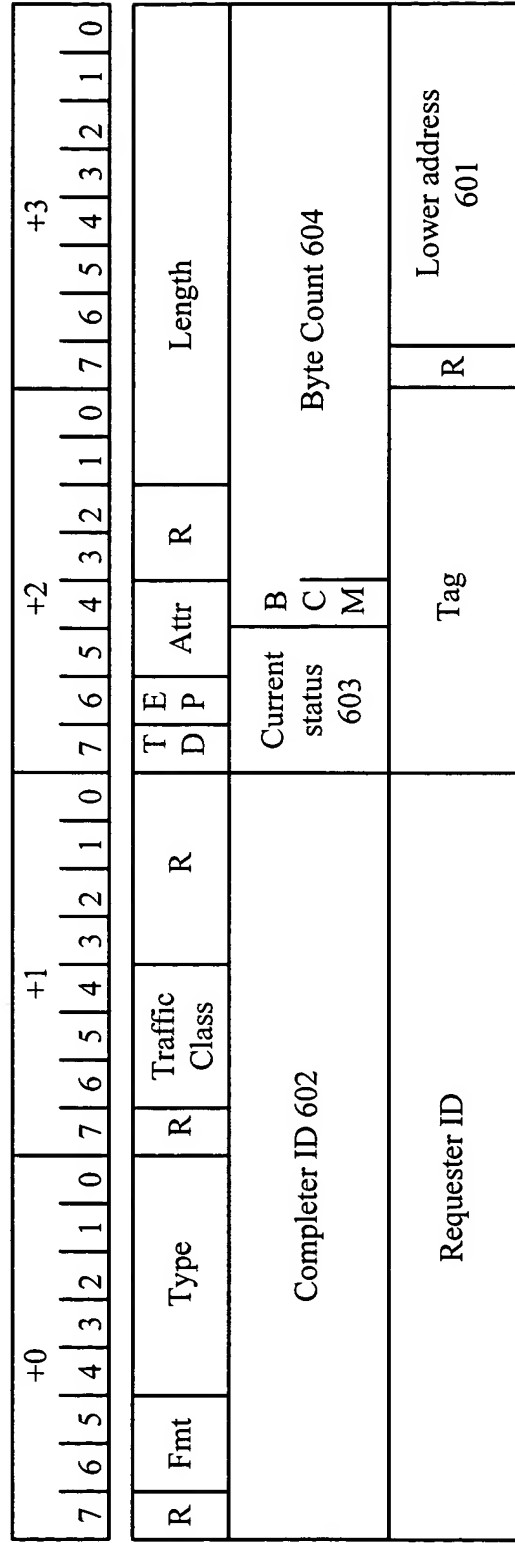


Figure 6

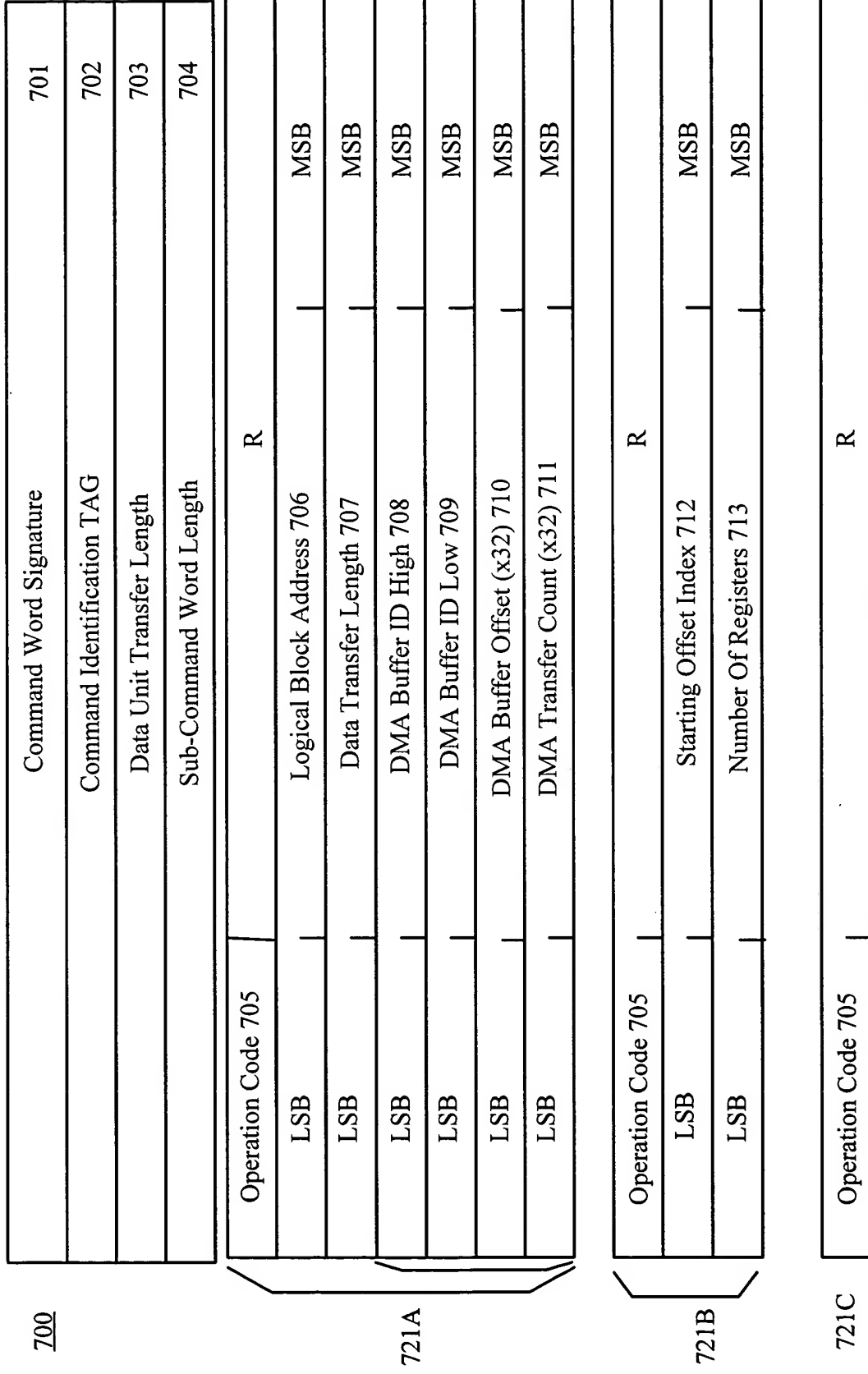


Figure 7

801	Sub- Command Name	Op Code	R/W	Description	Sub- Command Name	Op Code	R/W	Description	808
	Read Single	01 h	R	Read single sector	Erase Flash	1 fh	W	Erase flash by address	
802	Read Long	02 h	R	Read sector w/ a spare field	Read Attribute	21 h	R	Read attribute regs	809
803	Read Multiple Sectors	03 h	R	Read sectors using multiple sector setting	Write Attribute	31 h	W	Write attribute regs	810
804	Write Single	11 h	W	Write single sector data	Reset Device To Idle State	ff h	W	Init target to idle state, no data phase needed	811
805	Write Long	12h	W	Write single sector & ECC data	Identify Drive	fe h	R	Requester asks information from flash device	812
806	Write Multiple Sectors	13 h	W	Write multiple sectors using multiple sector setting	Format	fd h	W	Format flash device	813
807	Write Verify	14 h	W	Write verify by a read after write					

Figure 8A

Data 820	Spare Bytes 821
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Figure 8B

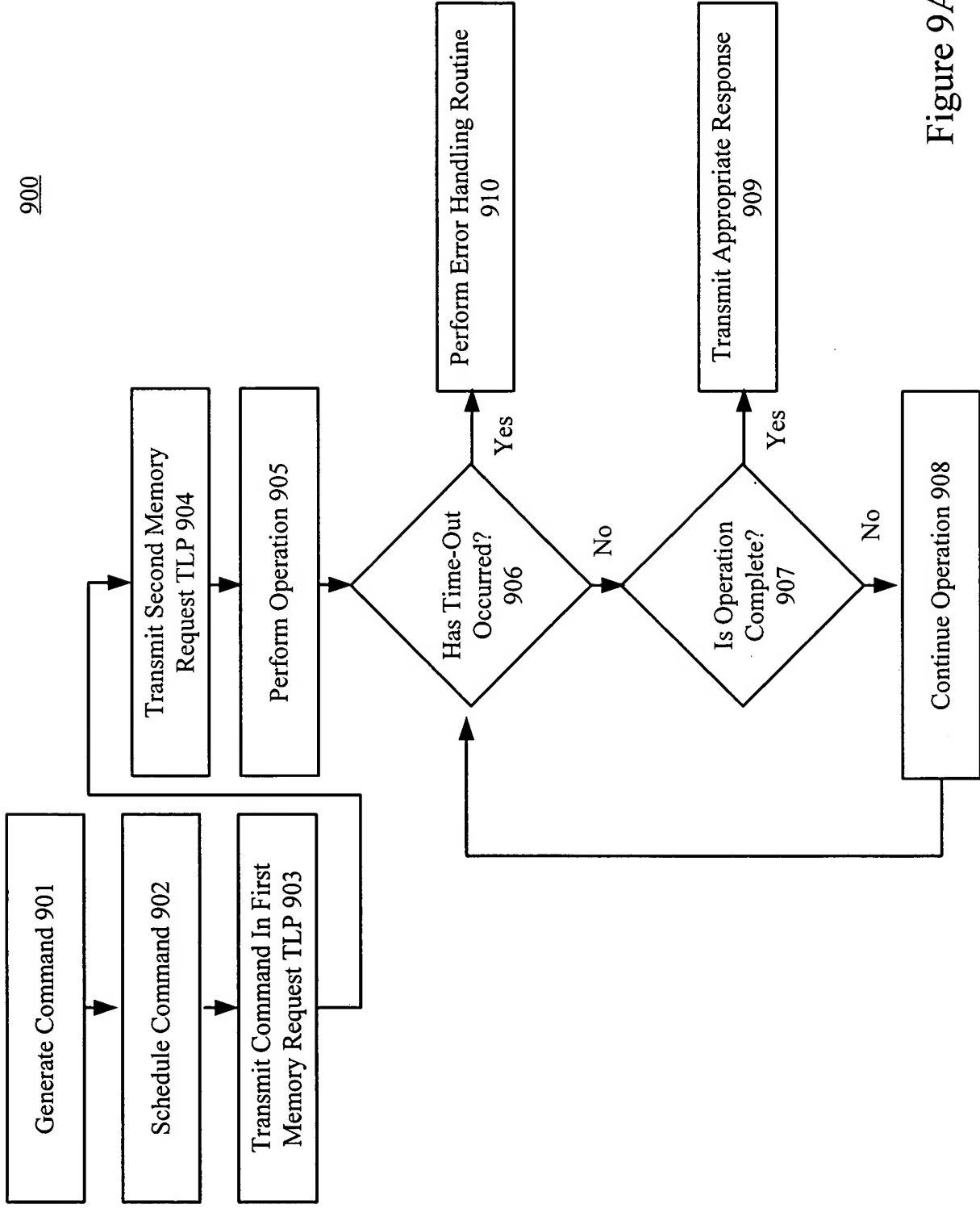


Figure 9A

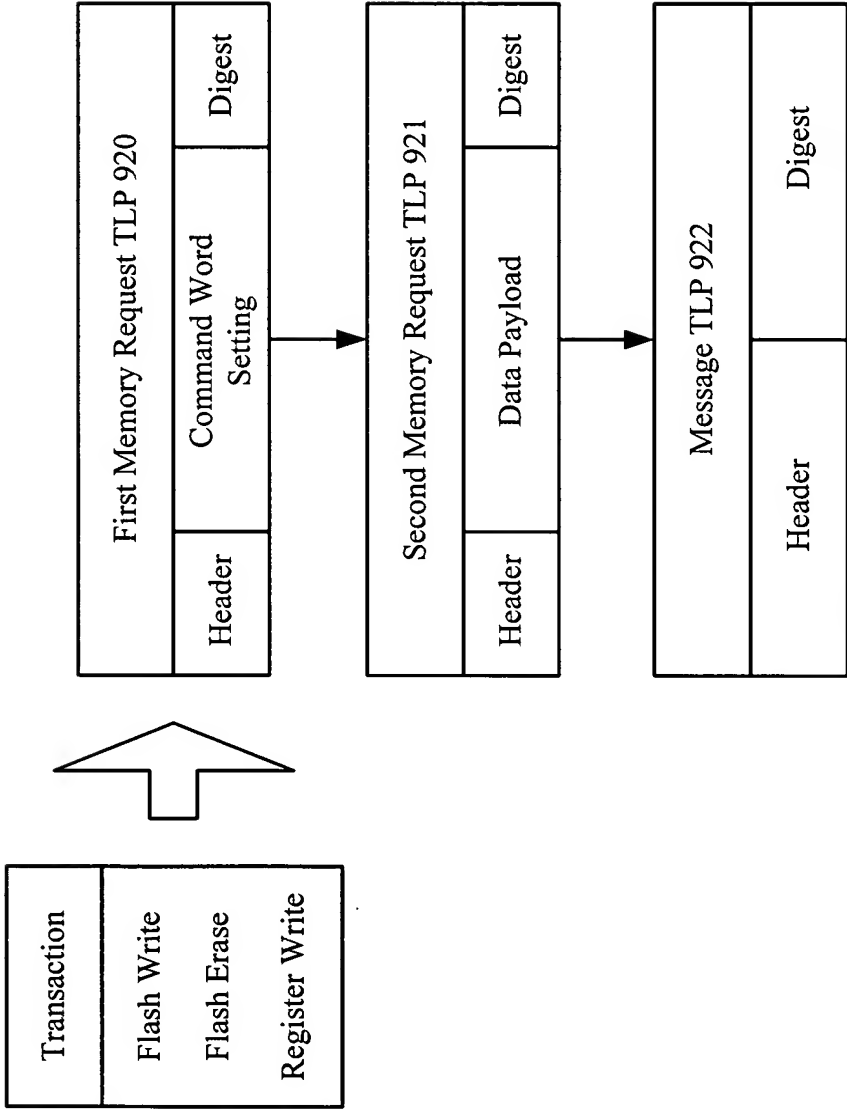


Figure 9B



Figure 9C

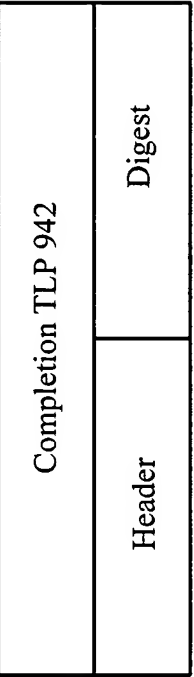
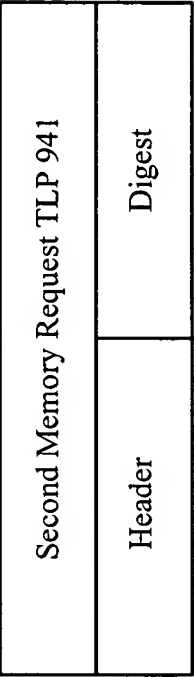
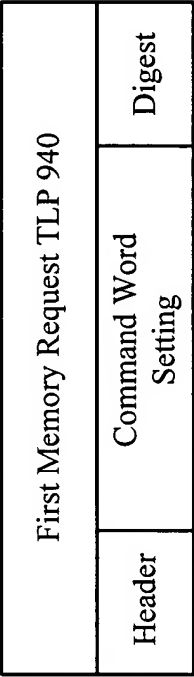
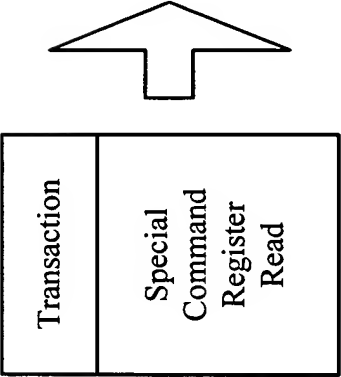


Figure 9D

1000

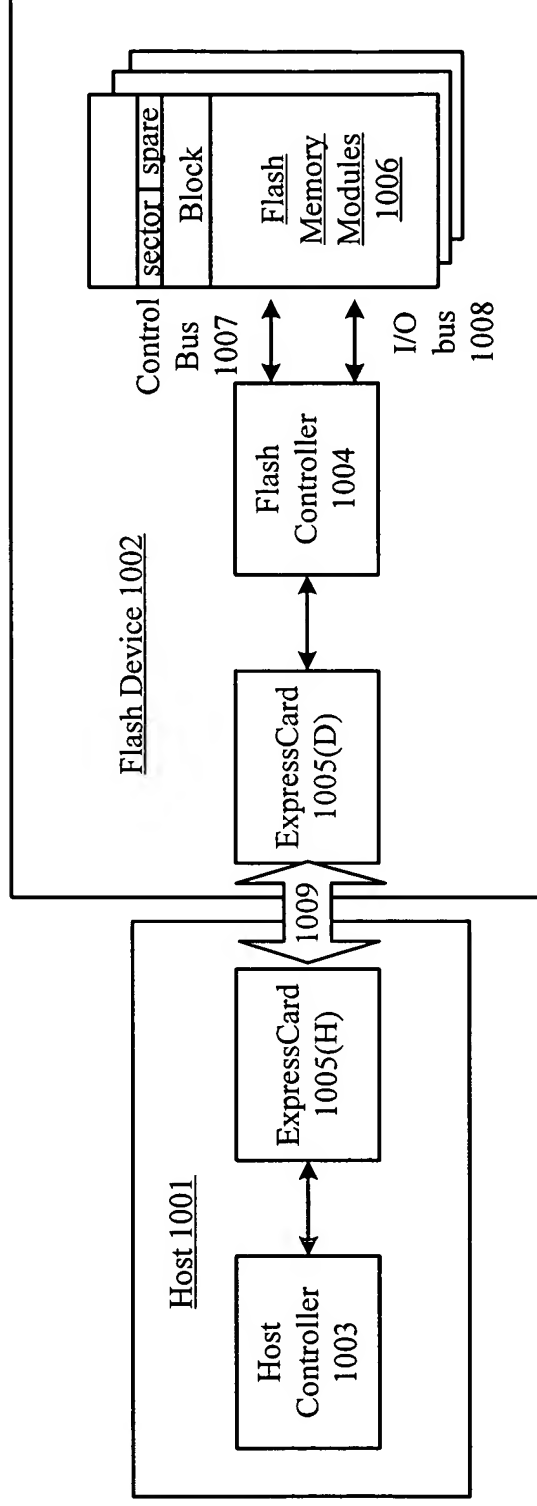


Figure 10

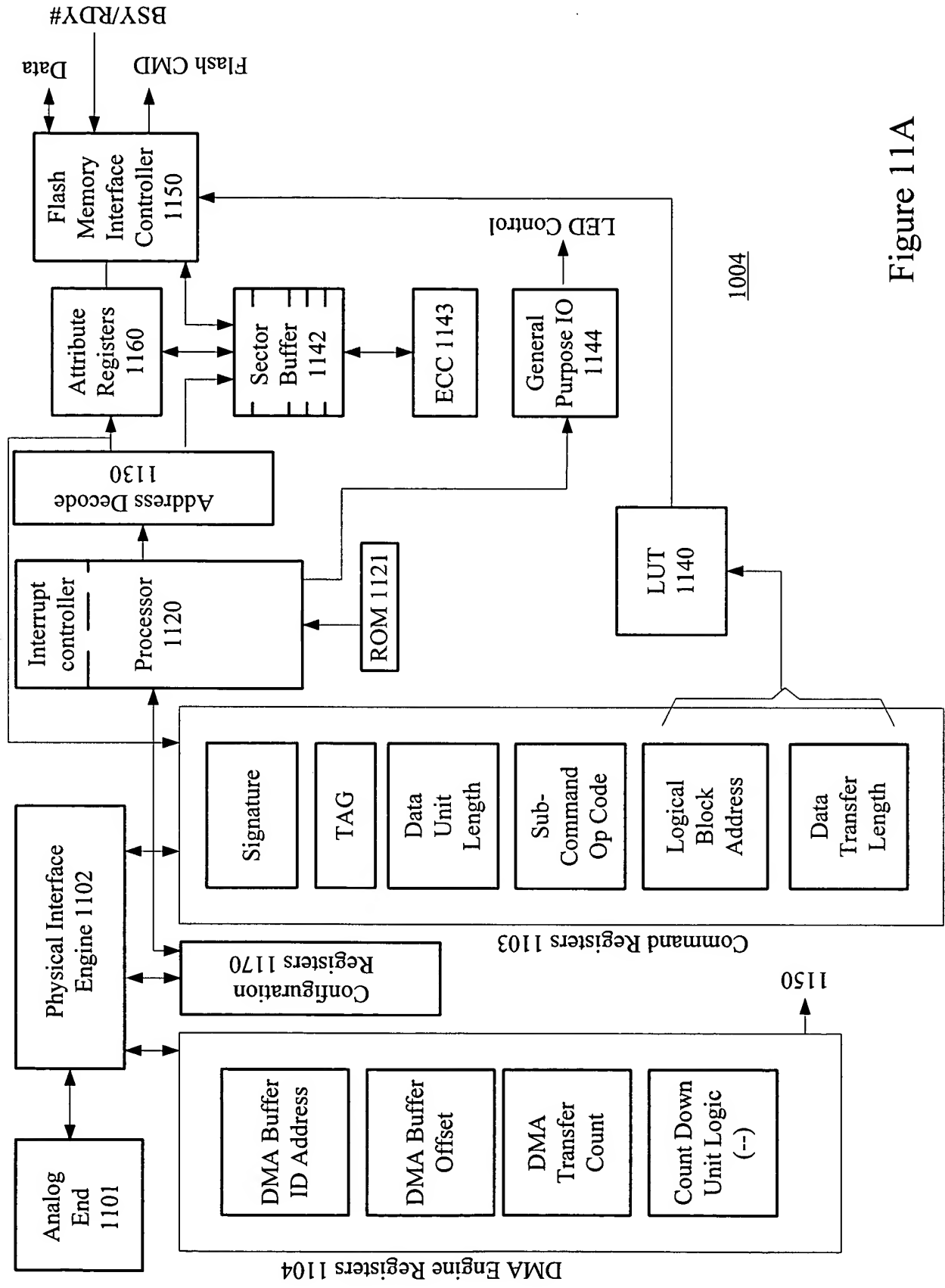


Figure 11A

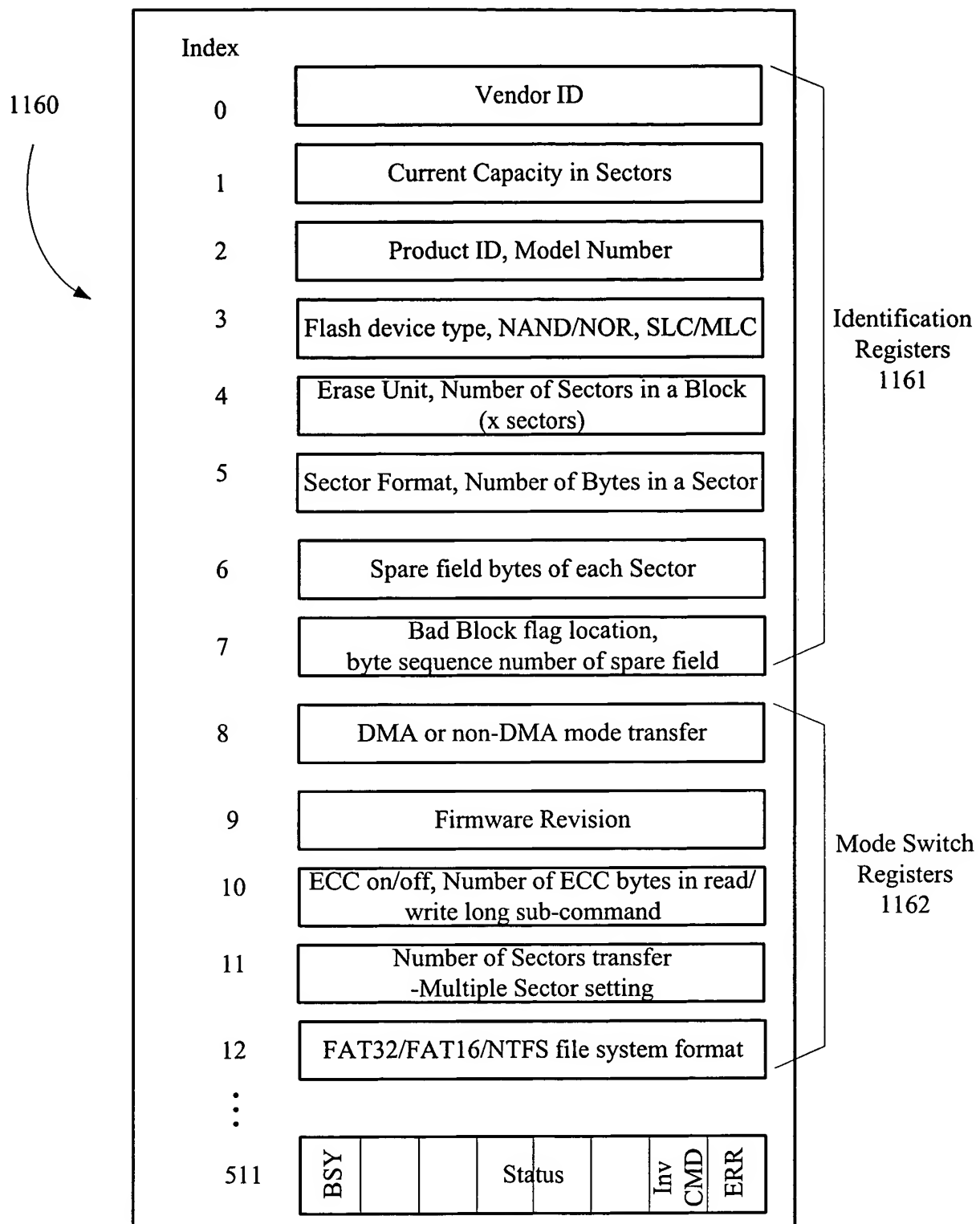


Figure 11B

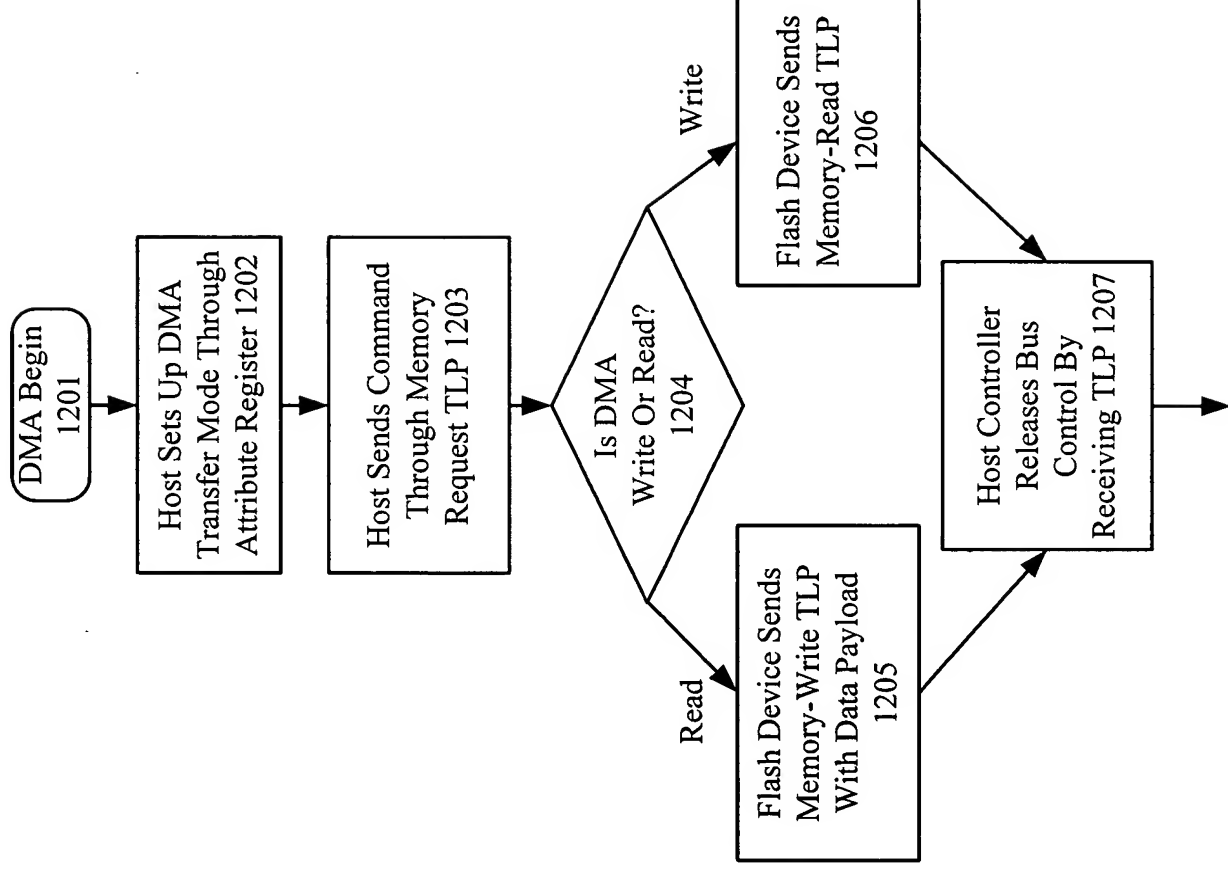


Figure 12A

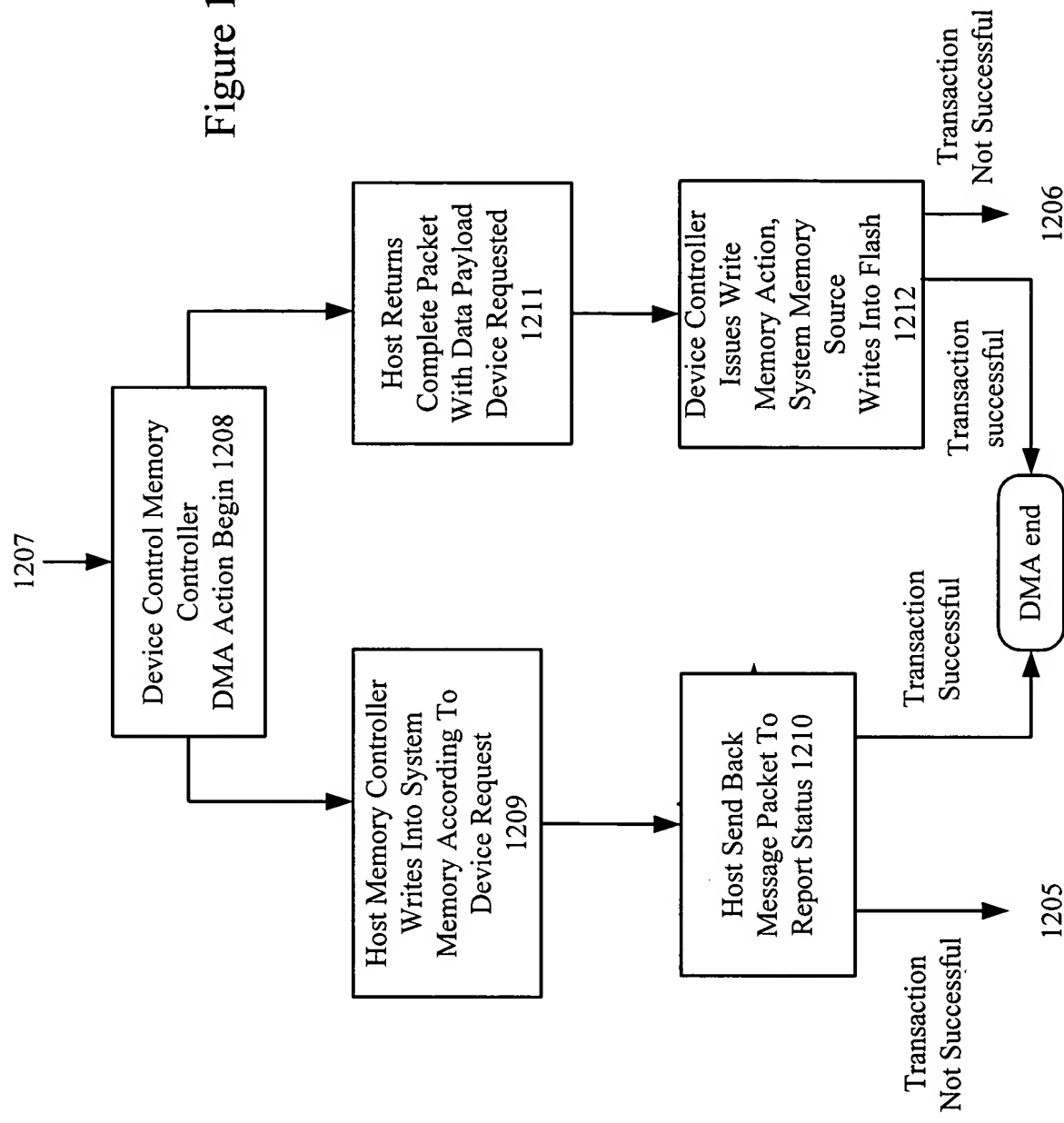


Figure 12B